

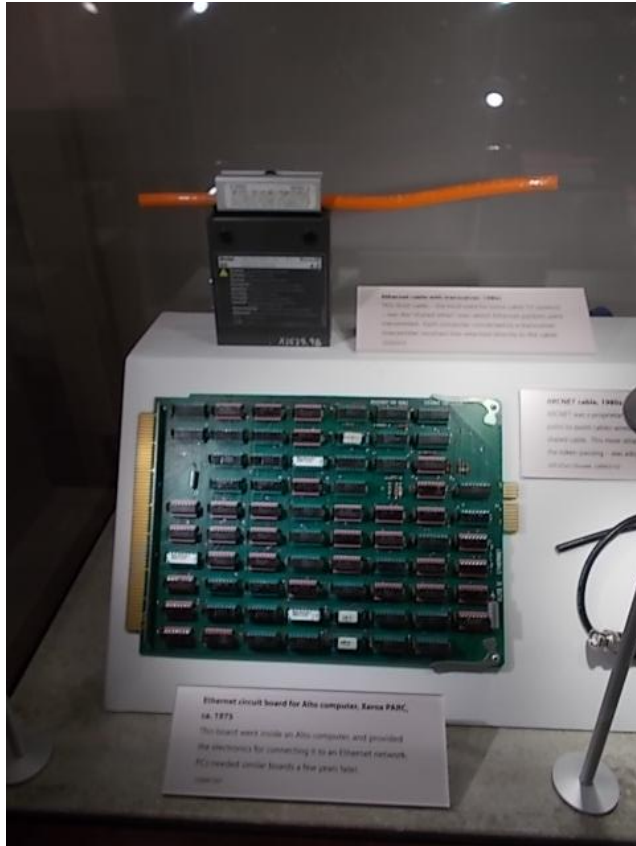
# FPGA ACCELERATED DATAPLANE

# Introducing myself: Mirek Walukiewicz

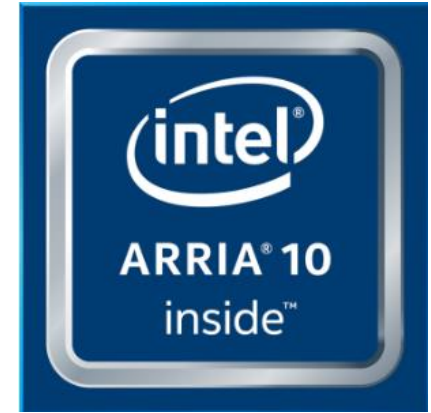
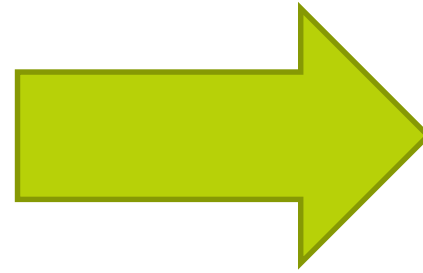


I'm from Gdansk, Poland.  
27 years in networking industry - 21 years working for Intel  
Now FPGA Solution architect  
Areas of interest:  
Virtualization  
I like looking for solution in unusual places.  
I'm married and I have two teenagers in place.  
My hobby is an history of art = galleries, ruins and ruins...

# FPGA for dataplane



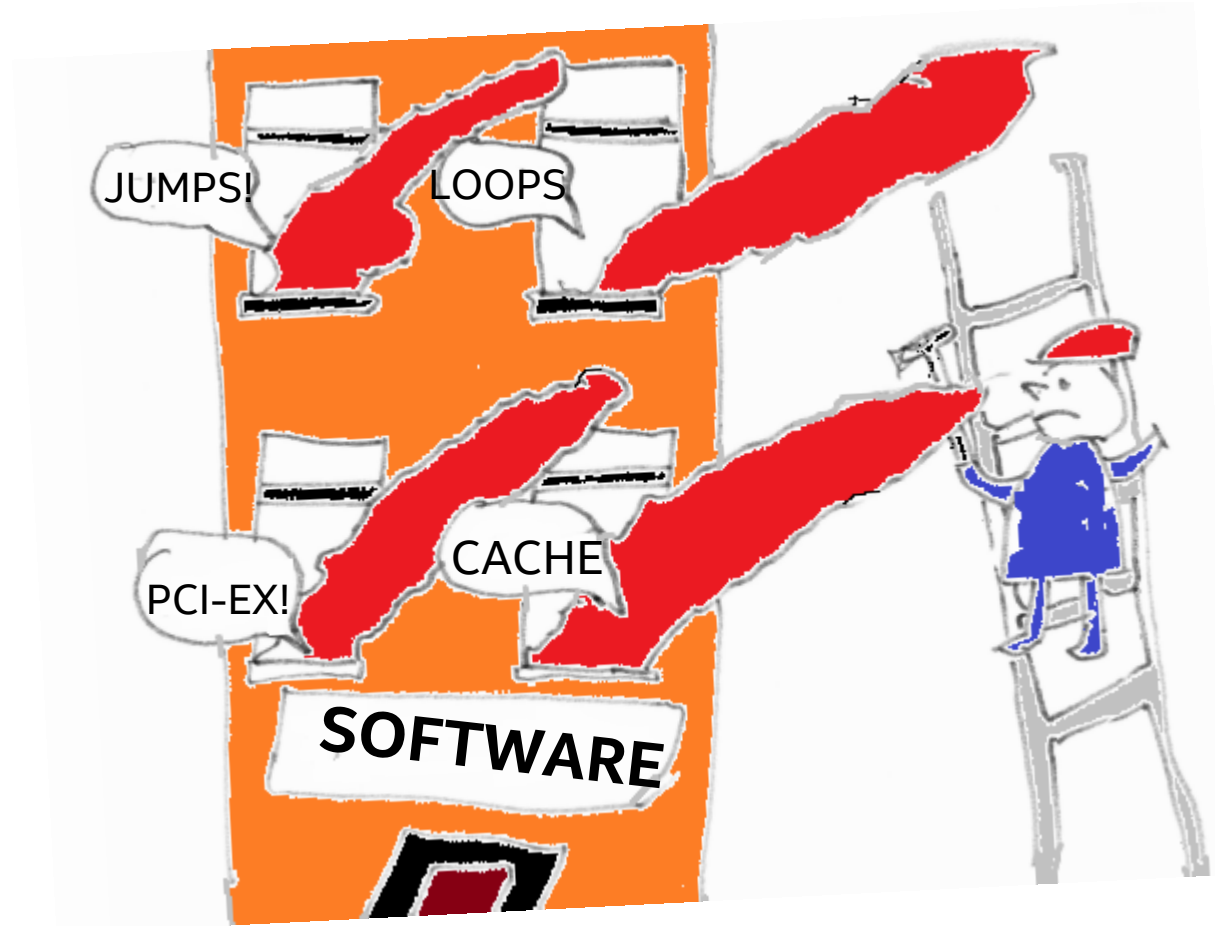
Thanks to Computer History Museum in Mtn View, CA



Thanks to Intel

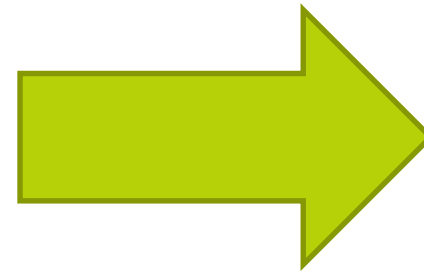


Find the hot spots first



FPGA designers DAILY JOB...

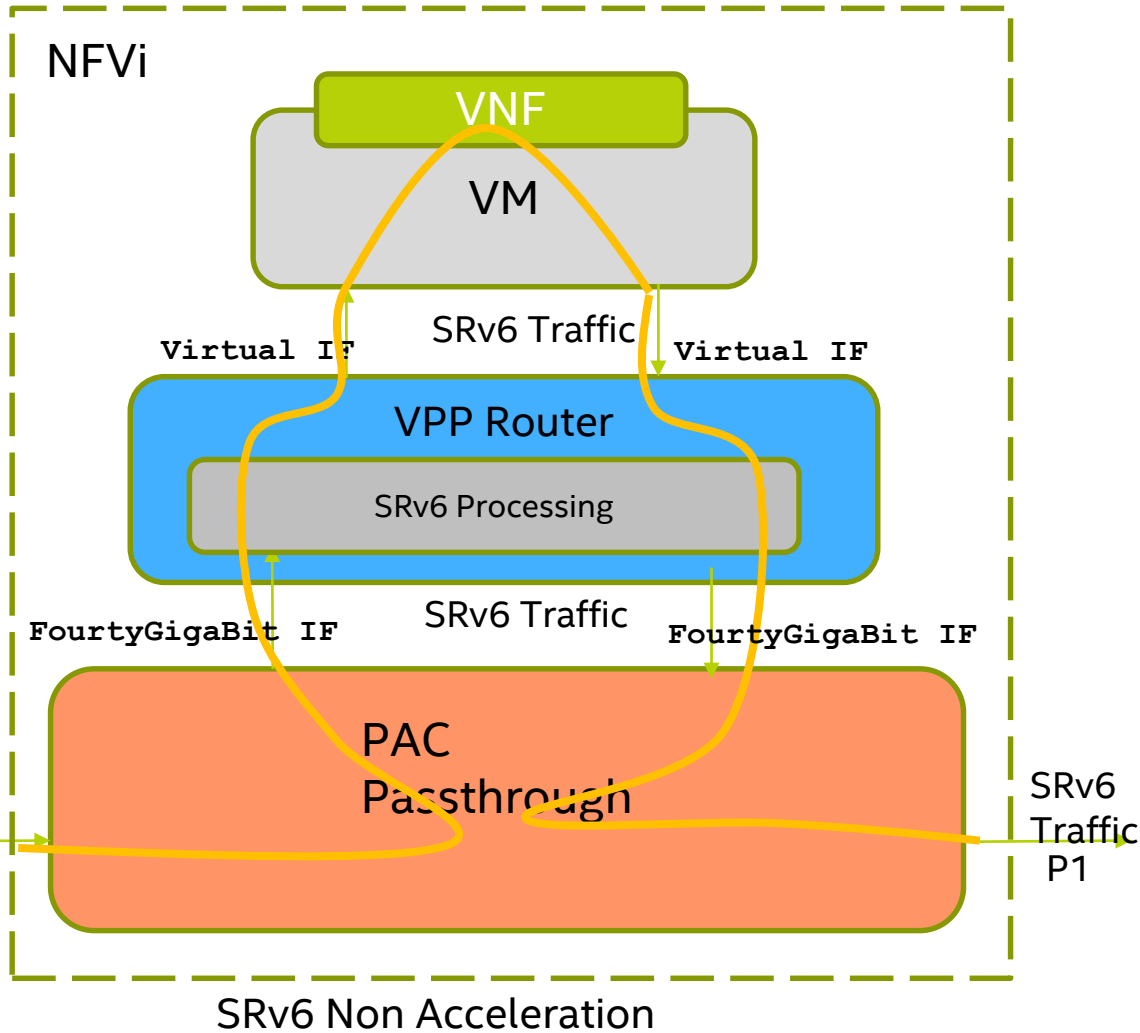
# Problem solving using FPGA



Put as much problem as possible to FPGA – it is flexible!  
Keep the rest of solution in SW.

# Example of the FPGA solution made by **HCL**

Non-Accelerated



Accelerated

