TASK SCHEDULING OF SDR KERNELS IN HETEROGENEOUS CHIPS OPPORTUNITIES AND CHALLENGES

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Outline

Part 1: The Hardware Specialization Era

-And its impact on SDR applications

Part 2: Task Scheduling on Heterogeneous Platforms

-STOMP: Scheduling Techniques Optimization in heterogeneous Multi-Processors

Part 3: New Scheduling Techniques

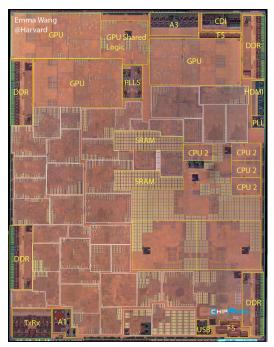
-Evaluation and future work



The Hardware Specialization Era Is Already Here...

- Heterogeneous system-on-chips (SoCs) are single chips comprising of many processing elements (PEs) of different nature like CPUs, GPUs and hardware accelerators
- Heterogeneous SoCs are extensively used today
 - Adopted by domains historically dominated by homogeneous architectures
 - Exploit heterogeneous characteristic of applications
 - Significant performance and power efficiency gains

Conventional schedulers are **not optimized** for the characteristics of heterogeneous chips which calls for more intelligent and efficient scheduling

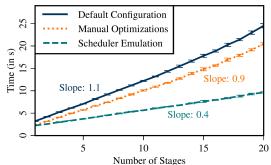






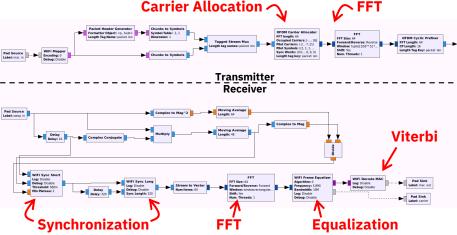
SDR and the Impact of Specialization & Task Scheduling

- A typical SDR application may consist of multiple and disparate kernels
- The underlying hardware may also provide accelerators for some or all of them
- However, in frameworks like GNU Radio, the scheduler mostly "ignores" these degrees of heterogeneity which may provide significant benefits when properly exploited



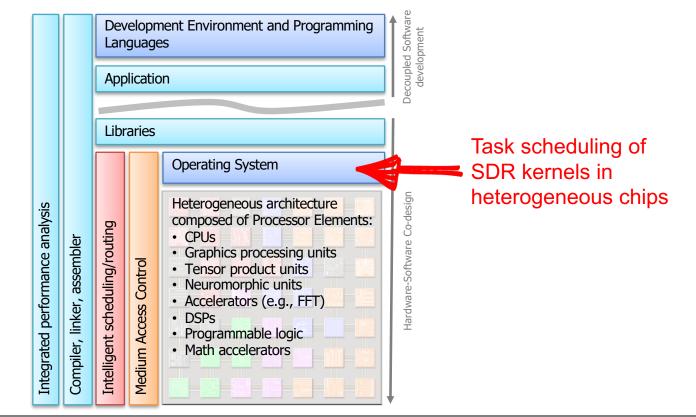
Prior works have shown that there is significant room for improvement in the GNU Radio scheduler
– E.g. via simple scheduling optimizations to increase cache effectiveness [1]

[1] B. Bloessl, M. Müller, M. Hollick. "Benchmarking and Profiling the GNURadio Scheduler." Proceedings of the 9th GNU Radio Conference. 2019



The Big Picture (Where Does This Talk Fit In?)

DSSoC's Full-Stack Integration





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Part 2: Task Scheduling on Heterogeneous Platforms



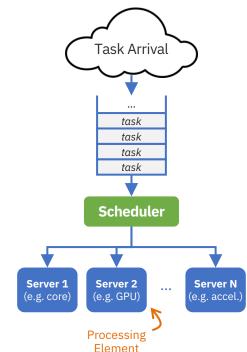
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Part 3: New Scheduling Techniques -Evaluation and future work



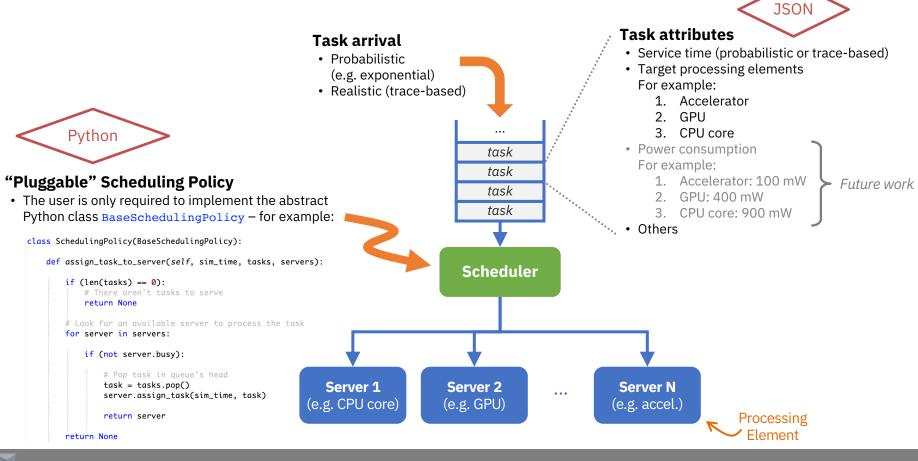
STOMP

- STOMP (Scheduling Techniques Optimization in heterogeneous Multi-Processors) is an opensource customizable Python-based simulator for fast prototyping of SoC scheduling policies – Check it out: https://github.com/IBM/stomp
- It consists of three main elements:
 - Tasks: units of work (aka jobs, threads, processes)
 - Executed in the heterogeneous SoC
 - Typically described as task types (e.g. *fft, decoder,* etc.)
 - Servers: processing units that can execute tasks
 - Different servers execute tasks with different "efficiency"
 - E.g. an FFT task on DSP accelerator vs general-purpose CPU
 - Scheduler: dynamically maps tasks to servers during the execution
 - It supports user-defined scheduler algorithms





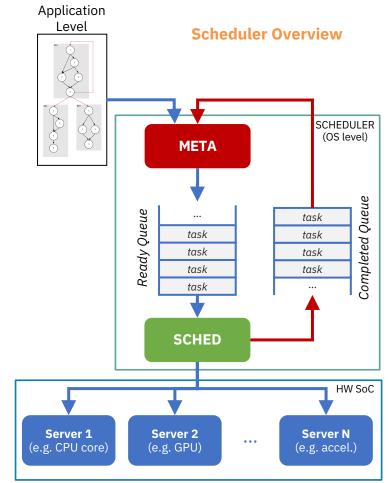
STOMP Overview



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STOMP Intrinsic Operation

- STOMP consists of two integral parts:
 - Meta scheduler ("META") → pre-processor that aids in the scheduling decision
 - Task scheduler ("SCHED") → assigns ready tasks to available servers (PEs) to optimize the overall response time
- META and SCHED communicate via two queues: *ready* and *completed*
- Input: directed acyclic-graphs (DAGs) of multiple tasks with associated real-time constraints (priority and deadline)

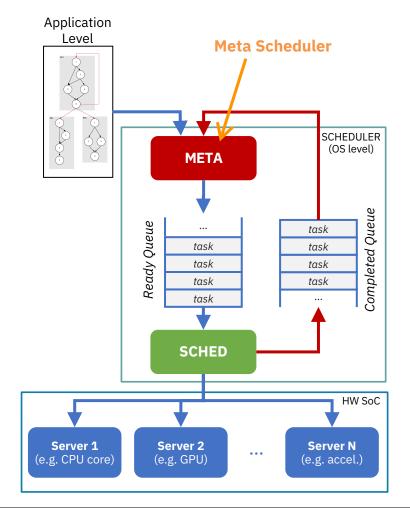


Meta Scheduler ("META")

- META tracks heuristics associated with the DAG:
 - Task dependencies, DAG deadline and available slack, DAG and tasks priority
- Then orders ready tasks based on a "rank"
 - Can be computed in different ways
 - For example, as a function of task's priority, slack and worst-case execution time (WCET)

 $Rank_{i} = \frac{Task_{i} Priority}{Task_{i} Slack - Task_{i} WCET}$

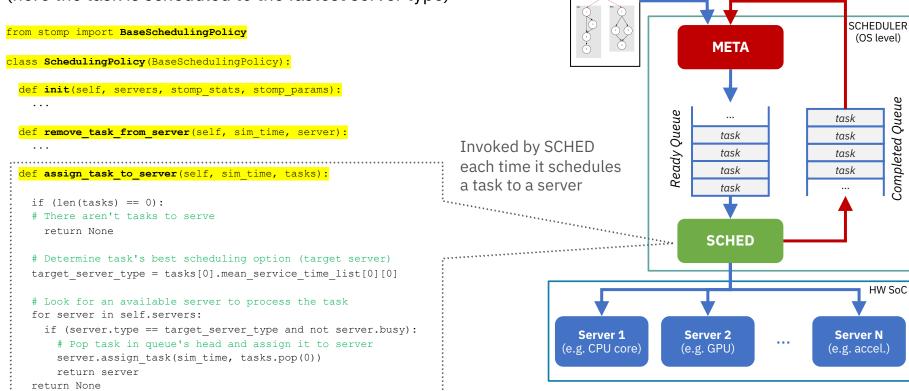
- Drops non-critical priority DAGs if deadline is missed
 - All remaining tasks in the DAG are dropped
 - Help reduce task traffic in the system





Task Scheduler ("SCHED")

The user primarily defines the assignment actions: (here the task is scheduled to the fastest server type)



Application Level

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Queue

Completed

Simulation Parameters and Configuration

Example stomp.json configuration file:

"INFO",

0,

".",

"",

"",

11.11

false,

```
"general" : {
   "logging level":
   "random seed":
   "working dir":
   "basename":
   "pre gen arrivals":
   "input trace file":
   "output trace file":
},
```

```
"simulation" : {
```

```
"max tasks simulated": 10000,
"mean arrival time":
"distribution":
"power mgmt enabled":
"max queue size":
```

"sched policy module": "policies.simple policy ver3", 50, "Poisson", false, 1000000,

```
"servers" : {
         "cpu core" : { "count" : 8 },
         "gpu": { "count" : 2 },
         "fft accel" : { "count" : 1 }
     },
"tasks" : {
         "fft" : {
             "mean service time" : {
                 "cpu core" : 500,
                 "gpu" : 100,
                 "fft accel" : 10
             },
             "stdev service time" : {
                 "cpu core" : 5.0,
                 "qpu" : 1.0,
                 "fft accel" : 0.1
         },
```

. . .

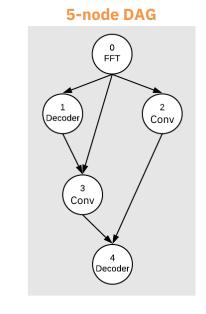
Example Using a Simple DAG

• Input: priority-1 5-node DAG with varying kernels — Deadline of DAG is set to 1100 units of time

• Time 0: META pushes *Task 0* to *ready queue* with a rank

 $Rank_{i} = \frac{Task_{i} Priority}{Task_{i} Slack - Task_{i} WCET} DAG's priority$ $Rank_{0} = \frac{1}{500 - 500} = \infty$

- Task 0 completes execution in 10 units of time because it was run on the accelerator
 - META then calculates the remaining slack of the DAG and next available tasks



Task	CPU	GPU	Accel
FFT	500	100	10
Convolution	200	150	10
Decoder	200	150	None

Tasks' Execution Times



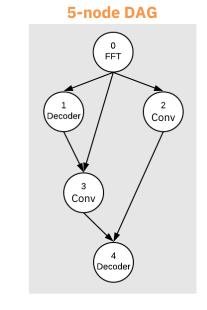
Example Using a Simple DAG (cont'd)

• Time 10: *Task 1* and *Task 2* become ready

- Scheduled in the order of their rank
- Task 1 has a higher rank than Task 2
 - $Rank_1 = 1/(363-200) = 1/163$
 - $Rank_2 = 1/(545-200) = 1/345$
- This process continues for all tasks in the DAG

Multi-DAG execution:

- Multiple DAGs arrive consecutively
- At every stage, ready tasks are scheduled in rank order across all DAGs



Task	CPU	GPU	Accel
FFT	500	100	10
Convolution	200	150	10
Decoder	200	150	None

Tasks' Execution Times



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–Evaluation and future work



Evaluation

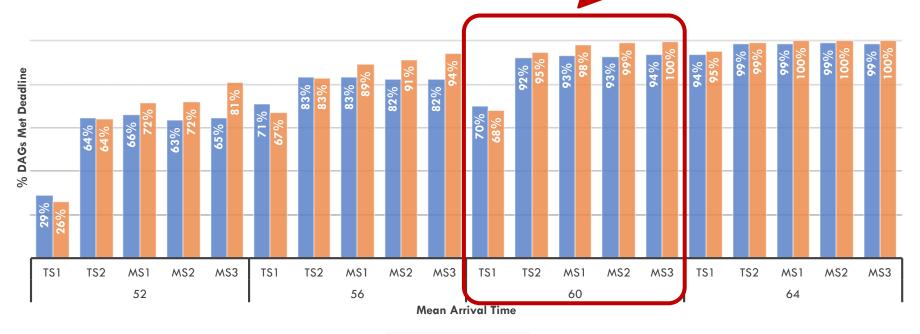
- DAG trace: 1,000 5- and 10-node static DAGs
 - Priority: 1 or 2 assigned randomly
 - Deadline: critical path length considering worst-case execution times
- Task types:
 - FFT, Convolution, Decoder
- Metric of evaluation:
 - Met deadline

Task	CPU	GPU	Accel
FFT	500	100	10
Convolution	200	150	10
Decoder	200	150	None

- Baseline task schedulers with META dependency tracking only
 - TS1: non-blocking task scheduler
 - TS2: non-blocking task scheduler assuming tasks ahead in queue are scheduled
- TS2 scheduler with both META dependency tracking and pre-processing
 - MS1: rank based on task's deadline and average execution time, and priority
 - MS2: rank based on task's deadline and maximum execution time, and priority
 - MS3: rank based on task's available slack and maximum execution time, and priority

Evaluation: Met Deadline

MS3 meets deadline for 33% and 5% more tasks than TS1 and TS2, respectively



Priority 1 Priority 2



Running STOMP

ipper 00:44 ~/research/IBM/STOMP/stomp_clean:						

Summary and Path Forward

- STOMP is in active development with a number of additional items being worked on - More complete input trace format, more statistics and data about the runs
- And there are some extensions planned
 - Power consumption models and power management features
 - Machine learning-based scheduling policies
- And work to move from the abstract to the more concrete - Analysis of GNU Radio workloads to generate more realistic DAG traces
- But STOMP already provides plenty of opportunity and capability to explore the problem space - readily available now:





Thank You!



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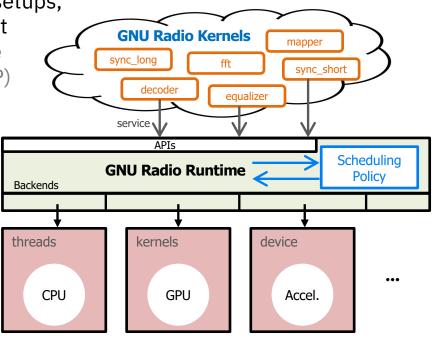
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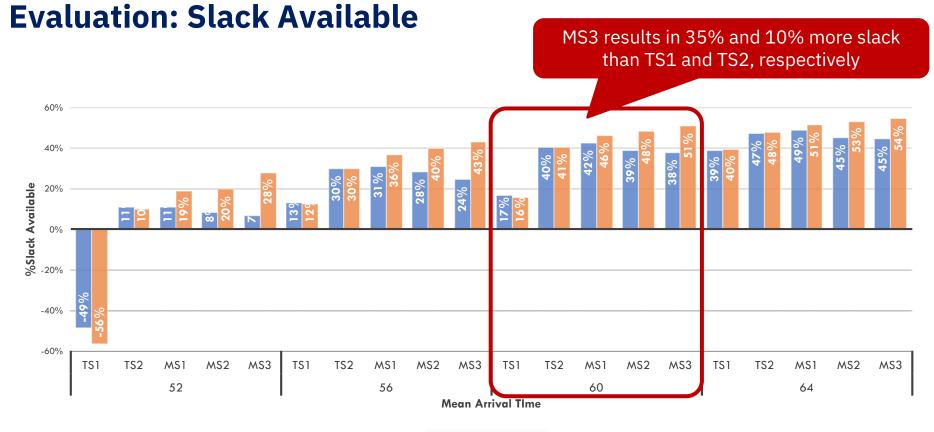
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Smart Scheduler Roadmap and Big Picture

- STOMP is only intended for early-stage evaluation of smart scheduling policies
- Ultimately these policies should be ported to real setups, e.g. as part of the GNU Radio run-time environment

 – GNU Radio makes run-time decisions using the specified policy (originally developed in STOMP)
- We can also use existing software middleware frameworks (e.g. OpenCL, OpenMP, OpenSSL) to prototype scheduling policies
 - Target architectures: IBM P9, NVIDIA Xavier



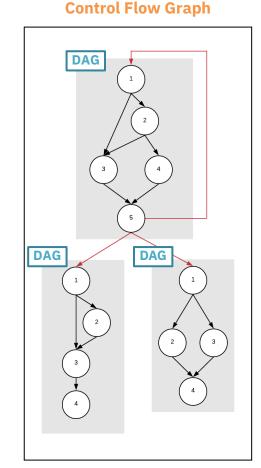


Priority 1 Priority 2



STOMP Inputs

- Domain-specific applications → control flow graphs
- Control flow graphs are divided into directed acyclic-graphs (DAGs) of multiple tasks
 - Task: unit of work that can execute on a server (PE)
- DAG trace as input
 - Compile-time: applications are known and DAGs are static
 - Runtime: DAGs arrive dynamically with variable arrival rate
- Each DAG has real-time constraints associated to it
 - A priority and a deadline
 - Determined at run-time based on the environment and functions of each DAG



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Scheduling Mechanism

- When a DAG arrives, META pushes ready tasks to the ready queue ordered by rank
 - SCHED then schedules them onto servers (PEs)
- Once a task completes:
 - SCHED pushes it into the completed queue
 - Task ID and execution time are passed back to META
 - META pops the completed task and finds its parent DAG
- META checks for resolved dependencies and finds ready tasks, then:
 - Calculates deadline of the new ready tasks
 - Assigns new priority based on the remaining slack
 - Updates rank of ready tasks and re-orders them
 - If remaining slack is negative and task has non-critical priority, drops the DAG

