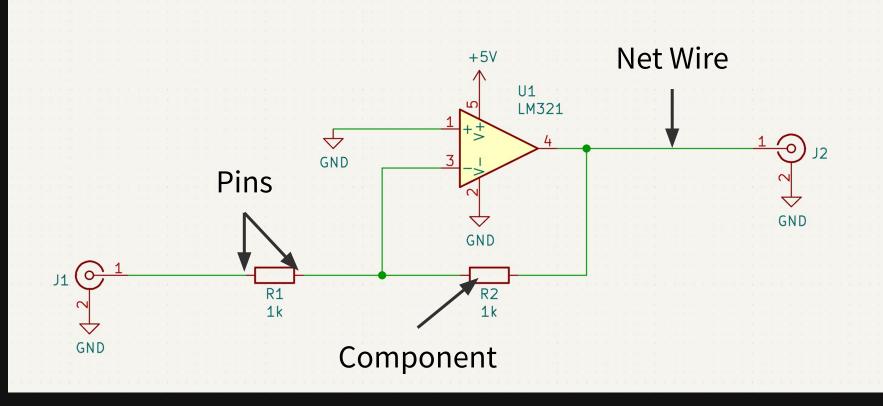
# Real-time Netlisting in KiCad

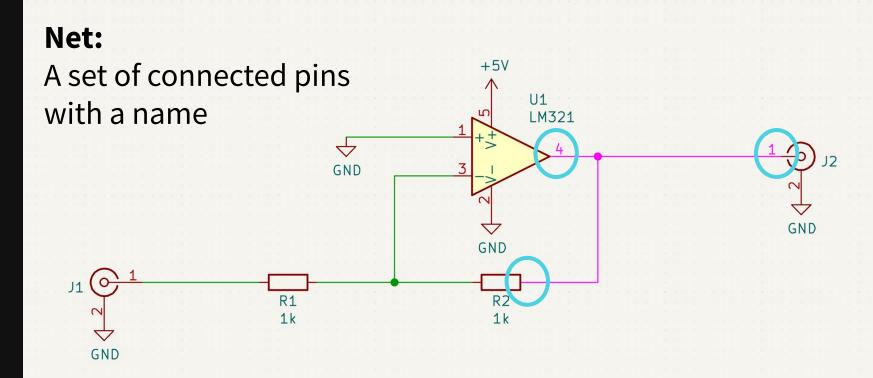
Jon Evans jon@craftyjon.com



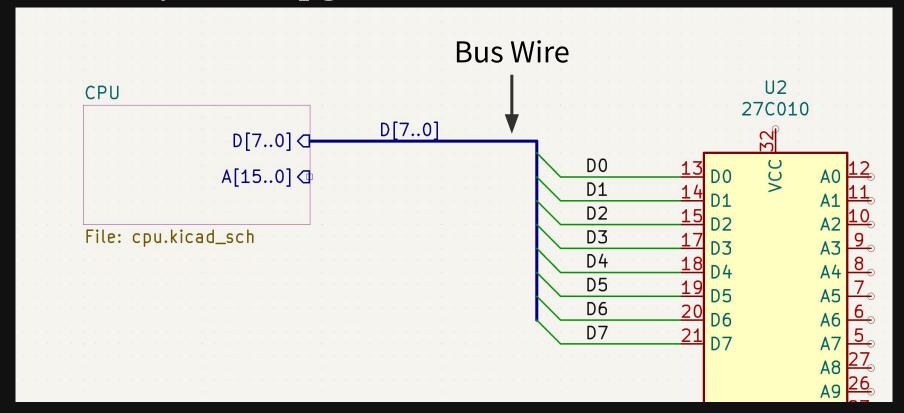
# Crash Course in Schematic Jargon



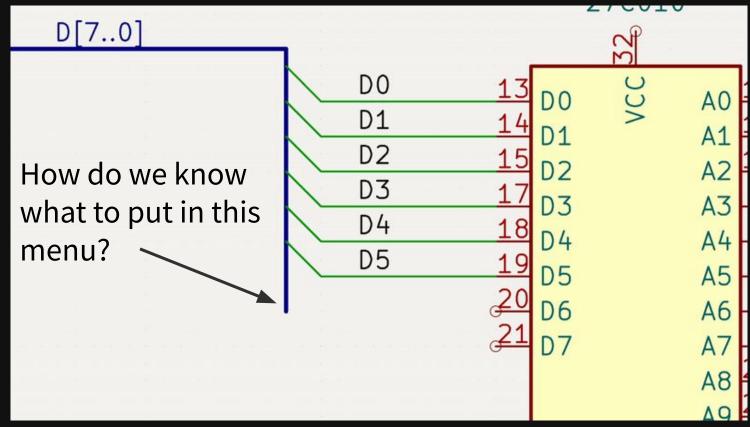
## Crash Course in Schematic Jargon



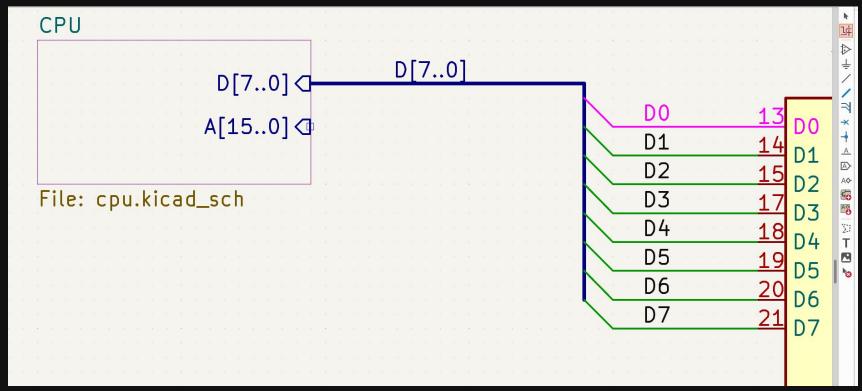
### Backstory: Bus Upgrades



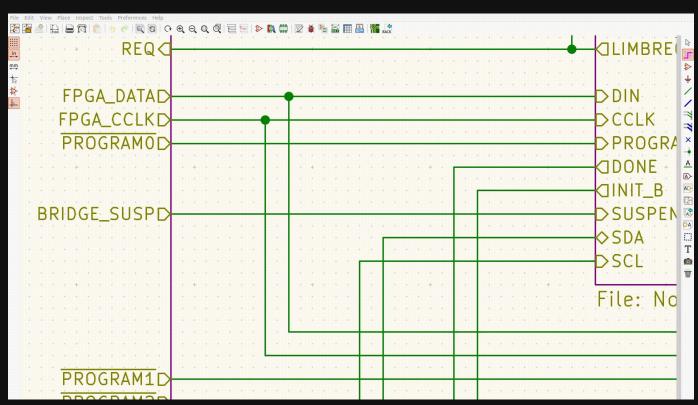
## **Bus Unfolding**



## But the Highlight tool already exists...?



#### This doesn't scale well...

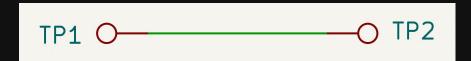




# Fast Real-time Netlisting in KiCad

#### Connectivity Algorithm at a High Level

1. What is graphically connected?



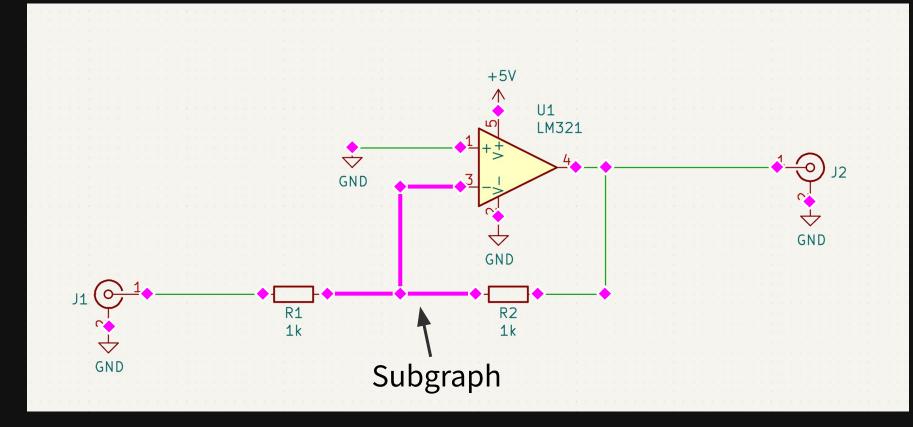
2. What is connected by labels?



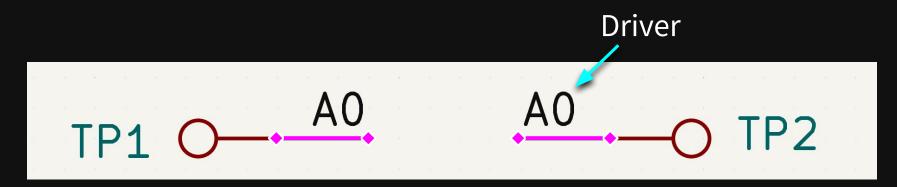
3. What about the hierarchy?



# What is Graphically Connected?



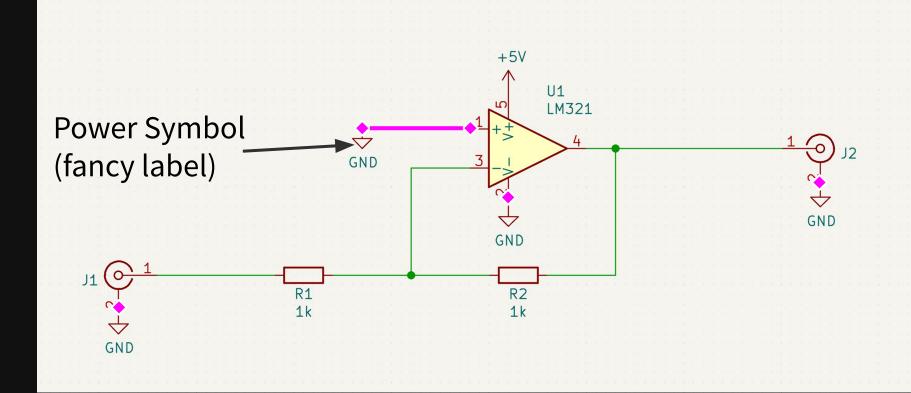
### What is Connected by Labels?



# Hierarchy of Drivers

Driver Type	Label Scope	Example	
Component Pin	Global	Net-(R1-Pad1)	
Hierarchical Sheet Pin	Local	/sheet/path/A	
Hierarchical Label	Local	/sheet/path/B	
Local Label	Local	/sheet/path/C	
Power Symbol Pin	Global	GND	
Global Label	Global	MY_NET	

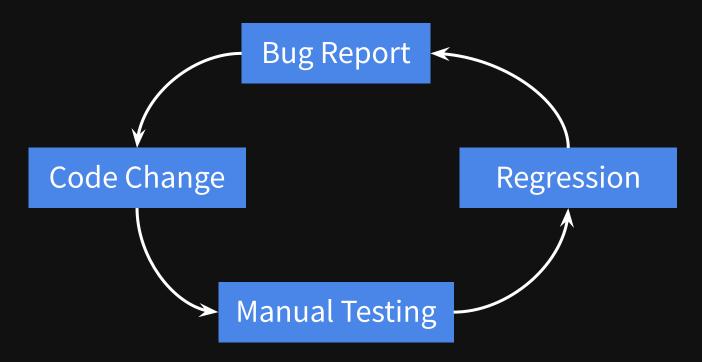
## What is Connected by Labels?



## Handling Hierarchical Designs

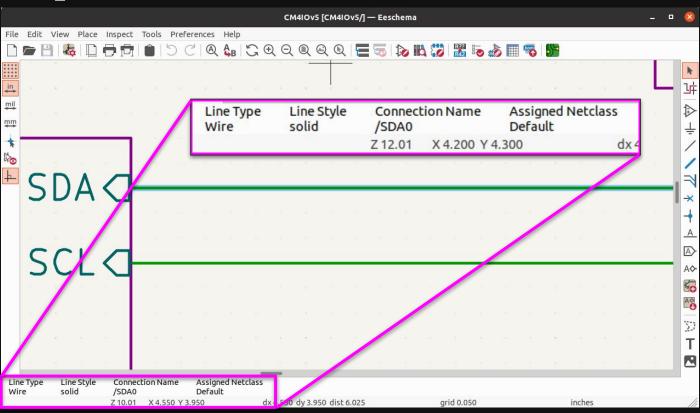


# Managing Complexity

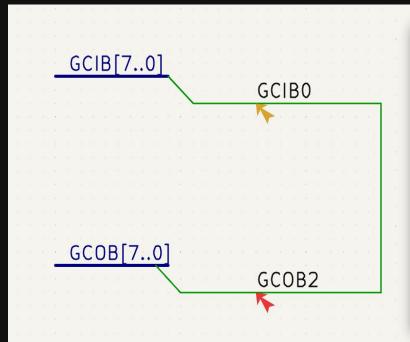


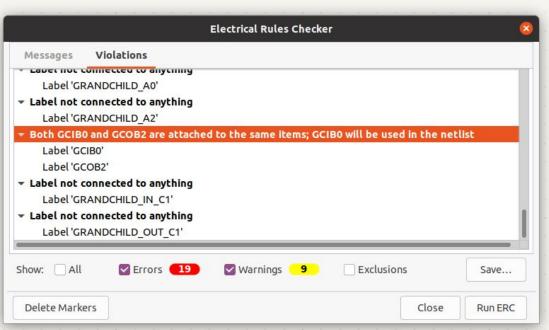
	KiCad 5.1	KiCad 5.99	Change
Design A 35 sheets	240 ms	57 ms	-76%
Design B 13 sheets	180 ms	81 ms	-50%
Design C 10 sheets	625 ms	150 ms	-76%

## Unanticipated Benefits



### Improving Electrical Rule Checks



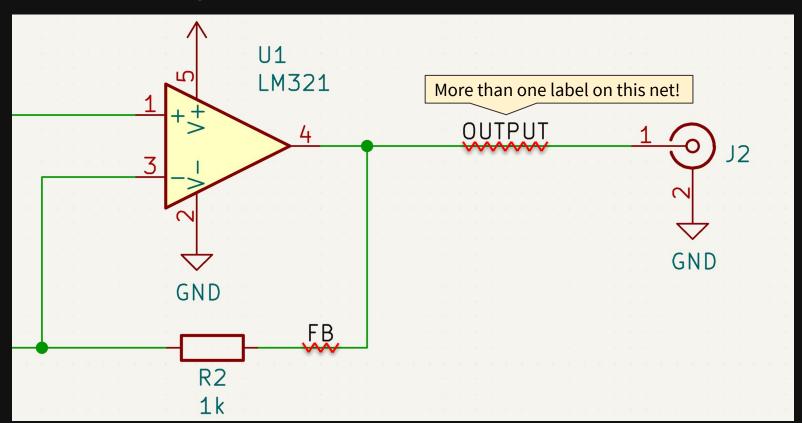


### Opportunities for Improvement

Make it even faster!

- Incremental updates
- Caching hierarchical sheet information
- Refactoring for performance

### Real-time ERC



# Q&A

Jon Evans jon@craftyjon.com

