# Cross-platform compilers with GNU Lightning



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#### https://gnu.org/software/lightning/

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## What is GNU Lightning?

- Code generator part of a JIT engine
- Virtual CPU, with virtual registers and virtual opcodes
- Cross-platform:
  - ARM, Alpha, HPPA, IA64, Loongarch, MIPS, PowerPC, RISC-V, s390, SPARC, SuperH, x86
  - 32-bit and 64-bit
  - Little-endian and big-endian
- LGPLv3, GNU C, no dependencies

## What is GNU Lightning?

- Created in 2000 by Paolo Bonzini
- JIT engine for GNU Smalltalk, and CLisp
- Moved to git in 2008
- Maintainer since 2012 is Paulo Cesar Pereira de Andrade
- Currently at version 2.2.3



- MIPS-to-everything JIT engine for Playstation 1 emulators
- Uses GNU Lightning
- <ad>See my talk tomorrow:

« Writing a dynarec, step by step »</ad>

## Why Lightning?

- LLVM / libgccjit don't box in the same weight class
  - They were designed for langages
    - High-level concepts (e.g. variables)
    - Language-focused algorithms
  - They were designed for ahead-of-time
    - Powerful optimizations
    - Generate very fast code... slowly
    - Lightning generates unoptimized code... very fast
- Different tools for different applications.

- *Minimum* of 6 GP + 6 FP registers:
  - 3 caller-saved:  $JIT_R0 \rightarrow JIT_R2$  (up to  $JIT_R(JIT_R_NUM)$ )
  - 3 callee-saved:  $JIT_VO \rightarrow JIT_V2$  (up to  $JIT_V(JIT_V_NUM)$ )
  - 6 floating-point: JIT\_F0  $\rightarrow$  JIT\_F5 (up to JIT\_F(JIT\_F\_NUM))
- Simple register allocator (that you don't need):
  - u8 reg = jit\_get\_reg(jit\_class\_gpr); jit\_unget\_reg(reg);

- « virtual opcodes » for binary operations, arithmetic, boolean, branching, memory I/O, floating-point math, function calls and function prolog/epilog
- Each instruction is composed of:
  - An operation, like « mul » or « sub »
  - Most times, a register/immediate flag (r or i)
  - A type identifier, when applicable.
    No type suffix = pointer-sized

#### jit\_<op><r|i>[\_<type>](01, 02, 03)

addr		f	d	01 = 02 + 03
addi		_f _f	d	01 = 02 + 03 01 = 02 + 03
addxr		-'	_4	01 = 02 + (03 + carry)
addxi				01 = 02 + (03 + carry)
addcr				01 = 02 + 03, set carry
addci				01 = 02 + 03, set carry
subr		f	d	
subi		f	d	01 = 02 - 03 01 = 02 - 03
subxr				01 = 02 - (03 + carry)
subxi				01 = 02 - (03 + carry)
subcr				01 = 02 - 03, set carry
subci				01 = 02 - 03, set carry
rsbr		f	d	$\begin{array}{rrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrr$
rsbi		f		01 = 03 - 01
mulr				$01 = 03 \times 03$
		-'-	-4	01 = 02 + 03
muli		_†	_d	01 = 02 * 03
hmulr	u			01 = ((02 * 03) >> WORDSIZE)
hmuli	u			01 = ((02 * 03) >> WORDSIZE)
divr		f	d	01 = 02 / 03
UTVI	_u	_'	_u	01 = 02 / 03

Forward branch	Backwards branch
jit_node_t *n;	jit_node_t *n1, *n2;
<pre>n = jit_beqi(JIT_R0, 0);</pre>	<pre>n1 = jit_label();</pre>
jit_patch(n);	n2 = jit_beqi(JIT_R0, 0);
	jit_patch_at(n2, n1);

- Function prolog / epilog: jit\_prolog() jit\_epilog()
- Code generation: void (\*fn)() = jit\_emit();
- Code disassembly: jit\_disassemble()

#### Example of generated code

#### « jit\_addr(JIT\_R0, JIT\_R1, JIT\_R2); »

x86\_64: lea (%r10,%r11,1),%rax

MIPS64: daddu v0,v1,t4

PowerPC32: add r28,r29,r30

SH4:

mov r2,r1 add r3,r1



# « Unfortunately, no one can be told what GNU Lightning is. You have to see it for yourself. »

#### Demo

- Imagine a scripting langage called « MindBlown »
  - 32K memory cells, each signed 32-bit
  - < / > : switches to the left/right cell
  - + / : increment/decrement value at current cell
  - [/]: repeat until the current cell is zero
  - . : Print the character of the current cell